



Application Serial No. 10/051,494  
Docket No.: SC11692ZP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: ) Examiner: G. Fourson III  
Passlack et al. ) Group Art Unit: 2823  
Serial No.: 10/051,494 )  
Filed: January 18, 2002 )  
For: ARTICLE COMPRISING AN )  
OXIDE LAYER ON A GaAs-BASED )  
SEMICONDUCTOR STRUCTURE AND )  
METHOD OF FORMING SAME )

<b>CERTIFICATION OF MAILING UNDER 37 C.F.R. 1.8</b>	
<p>I hereby certify that I have cause the documents indicated herein to be deposited with the United States Postal Service to Addressee under 37 CFR 1.8 on the date indicated below and address to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</p>	
<p>Date: January 23, 2004</p>	
<p>Signature:</p>	
<p>Printed Name:</p>	<p>Mary Schnaiter</p>

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## **RESPONSE AND AMENDMENT**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is being submitted in response to the Office  
Action dated September 23, 2003.

**Amendments to the Specification:** No amendments to the specification are included with this response.

**Amendments to the Claims** are reflected in the listing of claims which begins on Page 3 of this paper.